

Tutorial Schedule - 5 Annual e-VLBI Workshop, 17-20 September 2006, Haystack Observatory				
Sun, 17 Sep				
	9:00	1:30	Hobby/Carlson	ISO layer and TCP fundamentals
Break	10:30	0:30		
	11:00	1:30	Hughes-Jones	TCP tuning for long-distance high-bandwidth networks
Lunch	12:30	1:00		
	13:30	1:30	Hobby/Carlson	Performance - definition, tools, techniques
Break	15:00	0:30		
	15:30	1:30	Open discussion	Open discussion on transport, tuning, performance, motherboard, NIC, etc
Adjourn	17:00			
Mon, 18 Sep				
	9:00	1:30	Sobieski	Optically-switched networks
Break	10:30	0:30		
	11:00	1:30	Ruszczuk	VSI-E protocol fundamentals
Lunch	12:30	1:00		
	13:30	1:30	Whitney/Smythe/Cappallo	Mark 5B tutorial and demonstration
Break	15:00	0:30		
	15:30	1:30	Discussion, hands-on	Extended discussion; hands-on with Mark 5 tuning, Mark 5B; VSI-E, etc
Adjourn	17:00			

Program Schedule - 5th Annual e-VLBI Workshop, 17-20 September 2006, Haystack Observatory				
Tues, 19 Sep				
Intro	9:00	0:10		
	9:10	0:20	Tzioumis, Tasso et al	e-VLBI progress in the South
	9:30	0:20	Xiuzhong Zhang, team of Chinese VLBI N	e-VLBI progress in China
	9:50	0:20	Yun, Charles T.	EXPRoS
	10:10	0:20	Amy, Shaun	The ATNF Gigabit Wide-Area Network
	10:30	0:20	Durand, Steven J, Romney, Jon	The EVLA Radio Telescope Optical Digital Transmission System
Break	10:50	0:30		
	11:20	0:20	Garrett, Michael; Szomoru, Arpad; et al	e-EVN - an open, real-time VLBI Array - first Science
	11:40	0:20	Strong, Matt; Spencer, Ralph et al	ESLEA and e-VLBI Development at Jodrell Bank Observatory
	12:00	0:20	Szomoru, Arpad	e-VLBI developments at JIVE
Poster review	12:20	0:20		
Lunch	12:40	1:00		
	13:40	0:20	Whitney, Alan R.	e-VLBI Developments at Haystack Observatory
	14:00	0:20	Takaba, H., Kawaguchi, N., et al	Giga-bit Geodesy e-VLBI by using the Super-Sinet
	14:20	0:20	Roy, Alan; Alef, Walter; Graham, Dave	e-VLBI Progress at MPI
	14:40	0:20	Koyama Yasuhiro, Kondo T. et al	Developments for real-time software correlation e-VLBI
	15:00	0:20	Ying Xiang, Zhijun Xu	Real Time Correlator in FPGA
Break	15:20	0:30		
Panel	15:50	1:30	Ruszczczyk(chair)	Mark 5/e-VLBI Kernel and Distribution Upgrades & Related Issues
Adjourn	17:20	0:40		(Dudevoir, Himwich, Mujunen, Quick)
Dinner	18:00			
Wed, 20 Sep				
	8:40	0:20	Ritakari, Jouko; Mujunen, Ari; et al	Usus modernus pandectarum
	9:00	0:20	Sobieski, Jerry	Optical hybrid networks - what are they and why does e-VLBI care?
	9:20	0:20	Ruszczczyk, Chester	e-VLBI Software Suite and Optical Network API
	9:40	0:20	Sansa, Julianne; Szomoru, Arpad; et al	A Simulation model for e-VLBI traffic on network links in the Netherlands
	10:00	0:20	Hughes-Jones, Richard Kershaw, et al	Interactions between the Network Transport layer and the Application
	10:20	0:20	Casey, Simon; Hughes-Jones, Richard; e	VLBI_UDP - An application for transferring VLBI data via UDP protocol
Break	10:40	0:30		
	11:10	0:20	Kettenis, Mark; Szomoru, Arpad	Pluggable TCP Congestion Avoidance Modules for eVLBI
	11:30	0:20	Ruszczczyk, Chester	VSI-E non-real time e-VLBI Trials - NyAlesund to Haystack
	11:50	0:20	Takashima, Kazuhiro; Machida,Morito;	The current status of K5 eVLBI intensive session
Poster review	12:10	0:20		
Lunch	12:30	1:00		
	13:30	0:20	Lapsley, David	A practical guide to Network Security for e-VLBI
	13:50	0:20	Whitney, Alan R.	The Mark 5B VLBI Data System
	14:10	0:20	Werthimer, Dan, Chen, Henry, et al	General Purpose FPGA Boards and Libraries for Correlators, Spectrometers, VLBI Mark 5 Filter Banks and Disk Arrays
	14:30	0:20	Mujunen, Ari; Ritakari, Jouko et al	Towards 10 Gbps e-VLBI
Break	14:50	0:30		
Panel	15:20	1:40	Yun(chair)	Convergence of New Technologies with e-VLBI Goals

Adjourn	17:00		(Amy, Durand, Koyama, Lapsley, Sobieski)
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